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(concluded)

a plurality of logic circuits having either one of the same functions and different functions being mounted in each of said active regions, and
a selection circuit for selectively operating only one of said plurality of mounted logic circuits.

2. (Amended) The semiconductor device comprising:
an I/O region formed on a chip and having at least an input/output pad;
an active region formed on said chip;
a plurality of logic circuits having either one of the same functions and different functions being mounted in said active region, and
a selection circuit for selectively operating only one of said plurality of mounted logic circuits;
wherein said selection circuit includes a disconnecting section, and
said disconnecting section is disconnected to allow permanent setting of an operable circuit.

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4. (Amended) The semiconductor device according to claim 1, wherein said selection circuit selects a logic circuit to be operated on a basis of a signal input supplied from an outside through said input/output pad.

5. (Amended) The semiconductor device comprising:
an I/O region formed on a chip and having at least an input/output pad;
an active region formed on said chip;
a plurality of logic circuits having either one of the same functions and different functions being mounted in said active region, and
a selection circuit for selectively operating only one of said plurality of mounted logic circuits;

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wherein said selection circuit includes a transistor element connected in series with each said logic circuit between said logic circuit and a power terminal, and
said transistor element selects a logic circuit to be operated on a basis of a signal input supplied from an outside through said input/output pad.

IN THE ABSTRACT OF THE DISCLOSURE:

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ABSTRACT OF THE DISCLOSURE

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Please delete the present Abstract of the Disclosure and replace it with the following new

Abstract of the Disclosure:

A semiconductor device includes on the same chip at least an I/O region where an input/output pad is formed and active regions where a circuit can be mounted, where a plurality of logic circuits having the same functions or different functions are mounted in the active regions on the same chip.
